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In the paper there is presented a procedure which offers the possibility, when certain errors occur in a computer, to reconfigurate it, such that the correct computation mode to remain valid, although some components are eliminated. It is used the principle of Graceful Degradation, namely the damaged components are disactivated and their functions are transmitted to the components which remained intact. This disactivation takes place in Hardware low complexity components, for instance in main storage or in storage partitions.

It is shown that particularly for the main storage, it is thus possible to be obtained an increase of reliability.

The decrease of computer productivity caused by components disactivation are ignotable. The reconfiguration method presented in the paper is an interesting alternative for tolerance procedures of conventional errors.